METHOD FOR PCI EXPRESS POWER MANAGEMENT USING A PCI PM MECHANISM IN A COMPUTER SYSTEM

Abstract

A method for PCI Express Power Management using a PCI PM mechanism in a computer system. The computer system includes a PCI PME (Power Management Event) controller and a PCI Express Root Complex. The method includes converting a Beacon signal generated by the PCI Express Root Complex into a Pseudo-PME signal, the Beacon signal asserting the Pseudo-PME signal. A Pseudo-PME line electrically connected with an PME input of the PCI PME controller and the PCI Express Root Complex is provided for transmitting the Pseudo-PME signal to the PCI PME controller. The PME input receives PME signals generated by PCI-compliant devices through a PCI Bus of the computer system. Before the computer system is under the control of an operating system, the Pseudo-PME signal is de-asserted.